Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	9050	nonvolatile and eras$3	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/11/27 11:17
L2	2679	1 and sequence	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/11/27 11:17
L3	1936	2 and write	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/11/27 11:17
L4	18	3 and (voltage adj reset$3)	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/11/27 11:17
L5	3	4 and generator	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/11/27 11:19
L6	3	3 and (sub adj sequence)	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/11/27 11:20
L7	1829	3 and select$3	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/11/27 11:20
L8	3	7 and (power adj generator)	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/11/27 11:21
L9	272	7 and route	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/11/27 11:21
L10	0	9 and (erase adj controller)	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/11/27 11:22
L11	208	9 and controller	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/11/27 11:22
L12	171	11 and reset	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/11/27 11:22

L13	0	12 and (voltage adj reset$3)	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/11/27 11:23
L14	157	12 and switch	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/11/27 11:23
L15	69	14 and transistor	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/11/27 11:27
L16	50	15 and verify$3	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/11/27 11:36
L17	16	14 and (memory adj cell)	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/11/27 11:37
L18	3	12 and 365/185.18	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/11/27 11:37
L19	2	12 and 365/189.09	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/11/27 11:38